**Course Title:** Digital Electronics Laboratory

**Course Code:** EEE 312

**Instructor:** Sabrina Tarannum, Dept. of CSE, PUC.

**Experiment List**

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| **Rep.**  **No.** | **Name of Experiment** | **Date of Performance** | **Date of Submission** | **Remark** |
| 1 | Familiarization of the Digital logic gates ICs & truth table. |  |  |  |
| 2 | Verification of Fundamental of logic gates. |  |  |  |
| 3 | Boolean function implementation. |  |  |  |
| 4 | Boolean function implementation. |  |  |  |
| 5 | Verification of universality of NAND & NOR Gates. |  |  |  |
| 6 | Verification of Full-Adder circuit and Half-Adder circuit |  |  |  |
| 7 | Verification of basic operation of a Decoder. |  |  |  |
| 8 | Verification of thve operation of Multiplexer. |  |  |  |